



Our vision

*Ampere's vision is to deliver a new standard for cloud and edge servers
while resetting expectations
for lower power, higher performance and a better total cost of ownership.*

The world is moving its data to the cloud at unprecedented speed. The vast majority of what you access on devices today (music, photos, games, computing, enterprise apps, retail experiences) lives in a cloud somewhere. At Ampere, we believe it's time for a new hardware technology that is focused on software with greater speed and efficiency.

Our story

Like the scientist behind its name, Ampere employees are innovators. We understand the needs of cloud computing and different software requirements. We are inventing what comes next and looking at everything from the structure of memory and how efficient the system is, to considerations on speed, cost of electricity and ability to cool. Power, size, weight and cost are driving the technology requirements and the innovation to come.

Our world class team of engineers, with depth and expertise in the cloud and semiconductor industries, is not only focused on the development of new semiconductor designs but also building out the first software ecosystem for Arm®-based server processors. Through the Ampere approach to the cloud and edge, we give our customers the freedom to challenge the status quo and accelerate next-generation data centers for the most memory-intensive applications.

Given the challenge we have outlined, we are building a culture of young-spirited entrepreneurs that ensures customers come first, proactively approaching industry challenges in the areas of security, power and performance, and delivering results that matter most.

Products built for the cloud

Our eMAG cloud solutions deliver advantages through our high performance cores, high-speed connectivity, memory throughput, and enterprise grade reliability. These solutions raise the bar on retrieve-and-compute capability in an existing data center footprint while lowering power and operating costs substantially.

Processor Features

- A unique, new designed, high-performance custom core Arm® v8-A 64-bit server operating at up to 3.3 GHz specifically designed for cloud servers
- High-performance memory bandwidth and leading memory capacity for cloud workloads
- Highly-integrated mixed signal I/O including PCIe Gen 3, SATA Gen 3 and USB
- Custom high-performance on-chip fabric designed to connect CPUs, memory and I/O

**FOUNDED IN
OCTOBER 2017**

**Chief Executive Officer:
Renée James**

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- Enterprise-class features:
 - Enterprise-class RAS (reliability, availability and serviceability)
 - Full virtualization support
 - Integrated security features
 - Advanced power management
 - Arm server base system architecture (SBSA) and server base boot requirements (SBRR) standards compliance
 - Unified extensible firmware interface (UEFI) compliant BIOS
 - Enterprise-grade operating system support

The Ampere platform represents a completely new processor architecture tailored for the emerging growth of cloud computing and next-generation data centers. The platform provides a quick, out-of-box experience to get started with the deployment of any desired workload.

Platform Features

- 19" chassis with an evaluation board featuring a built-in power supply, DRAM memory, storage disks and networking
- Boot and power management firmware
- Arm Trusted Firmware (ATF)
- AMI® AptioV UEFI BIOS with support for all the device peripherals, VGA and a configuration GUI
- CentOS Operating System
- GCC and LLVM tool chains
- AMI® MEGARAC BMC firmware for baseboard management
- Built-in support for workload accelerators
- Documentation and collateral

Software ecosystem

Open source is a dominant driving force in software with datacenter and cloud ecosystems continuing to grow and evolve at unprecedented speeds and new technologies being introduced at an equally impressive rate. The software that runs the cloud doesn't have the requirements of the legacy enterprise allowing Ampere to design with a different point of view. Our hardware is being designed to take on these new technologies and cloud workloads, giving the industry a choice.

Ampere is working with communities like Packet, WorksOnArm and OpenJDK to build the software ecosystem, participating in open projects such as the Linux Kernel, gcc/llvm, OpenBMC and other cloud technologies in order to ensure broad compatibility with our eMAG platform.

Additionally, the Ampere Developer Program provides a community and resources including forums, documentation, video tutorials as well as the option to purchase the Ampere eMAG Development Platform and everything you need to get started.

Visit the developer site for more details: developer.amperecomputing.com



Experienced team

Renée James, Founder, Chairman and CEO

Renée James is a seasoned technology leader with large-scale, broad international operations experience. She is currently the chairman and CEO of Ampere Computing, a company she founded. Renée had a lengthy career with Intel Corporation where she was the president of the company until her departure in 2016. She is a current operating executive at the Carlyle Group.

Chi Miller, CFO and COO

Chi is the chief financial officer and chief operating officer of Ampere. Prior to joining Ampere, he was senior director of finance at Apple Corporation, where he supported the R&D group. Prior to Apple, Chi was with Intel for 25 years, where he held a variety of roles including platform engineering VP of finance, software and services VP of finance, M&A controller and server products controller.

Atiq Bajwa, Chief Architect

Atiq is a seasoned technologist with over 30 years of technical leadership experience in product R&D. Prior to joining Ampere, Atiq was VP and GM of product architecture at Intel, where he led the architectural definition and development of Intel's computing products for the data center, PC, workstation and ultramobile markets. Before joining Intel, Atiq was a member of the team that developed the 32000 family of microprocessors at National Semiconductor.

Rohit Avinash Vidwans, Executive Vice President of Engineering

Rohit brings over 25 years of experience from Intel where he developed microprocessors, graphics, media and supercomputing processors and platforms, and holds 8 microprocessor design patents. Notable projects that he worked on or led include Intel's™ first 8 and 10 core Xeon™ microprocessors for data center and enterprise servers and Intel's first multi-core phone and tablet SOCs based on the ATOM™ core.

Stephan Jourdan, Ampere Fellow

Stephan brings a wealth of experience in CPU, SoC and system architecture to Ampere. Prior to Ampere, he spent 21 years in product architecture at Intel, where he held a variety of technology and management roles including CPU core architect, chief architect of Intel's PC client and mobile device SOCs, and most recently he was an Intel fellow in the Infrastructure and Platform Solutions Group (IPSG) and served as the IPSG chief technology officer. Stephan is a prolific inventor, with over 50 patents to his name. Stephan received his PhD from University of Toulouse, France, and an MBA from University of Oregon.

Matt Taylor, SVP Worldwide Sales and Business Development

Matt is the senior vice president of Worldwide Sales and Business Development at Ampere. Matt has 20 years of sales and business development experience in the data center and semiconductor industry. Prior to joining Ampere, Matt was the VP of Sales and Business Development for the Qualcomm Datacenter group, where he was responsible for Qualcomm's first Cloud, OEM, and ODM design wins and revenue shipments of the Centriq platform. Prior to Qualcomm, Matt was with Intel for 15 years, where he held a variety of Sales, Marketing and Business Development leadership roles. Most notably Matt led Intel's sales campaign for Cisco's datacenter portfolio and then went on to develop and lead the Worldwide Amazon account team, growing Amazon into a multi billion dollar account for Intel.



Mauri Whalen, Vice President Software Engineering

Mauri Whalen is vice president of software engineering at Ampere. Her team leads and delivers software for tools/compilers, BIOS/UEFI, OS, hypervisors, middleware, application development and optimization for the Ampere™ eMAG platform. She also drives the developer program which allows developers to create and innovate on Ampere's hardware platform. Prior to joining Ampere, she spent more than a decade in Intel's Open Source Technology Center. Her most recent position was vice president of a core system development team, where she led efforts in open source software development across a range of technologies and market segments, including enterprise Linux, leadership in the Linux ecosystem, client Linux programs and related technologies.



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