Our vision

Ampere’s vision is to set the new standard for cloud and edge servers and reset expectations for high performance and a better total cost of ownership.

Our story

At Ampere, we believe it is time for a new hardware technology that is focused on software with greater speed and efficiency. Inventing what comes next requires a fresh look at everything from the structure of memory and how efficient the system is, to speed considerations, the cost of electricity and the ability to cool. Power, size, weight and cost are driving the technology requirements and the innovation to come.

Ampere employees are innovators with a deep understanding of the requirements of cloud computing and different software environments. Our world class team of engineers, with decades of experience and expertise in the cloud and semiconductor industries, is focused on the development of new designs and building the first software ecosystem for Arm®-based server processors. The Ampere approach to the cloud and edge gives customers the freedom to challenge the status quo and accelerate next-generation data centers for the most memory-intensive applications.

With an entrepreneurial spirit, our engineers are addressing industry challenges in the areas of security, power and performance and are committed to delivering results that matter most to our customers.

Products built for the cloud

Our Ampere eMAG™ cloud solutions deliver advantages through our high-performance cores, high-speed connectivity, memory throughput, and enterprise grade reliability. These solutions raise the bar on retrieve-and-compute capability in an existing data center footprint while lowering power and operating costs substantially.

The Ampere eMAG platform offers significant total cost of ownership (TCO) value with competitive performance per dollar and performance per watt for high-volume mainstream servers used for edge, storage and web applications. The Ampere eMAG processor is the first in our multi-generation, high-performance product roadmap.

Ampere eMAG Processor Features:

- 32 Ampere-designed Armv8-A cores running up to 3.3 GHz Turbo
- Eight DDR4-2667 memory controllers
- 42 lanes PCIe 3.0 for high bandwidth I/O
- 125W TDP for maximum power efficiency
- TSMC 16nm FinFET process
Software ecosystem

Open source is a dominant driving force in software with datacenter and cloud ecosystems continuing to grow and evolve at unprecedented speeds, and new technologies being introduced at an equally impressive rate. The software that runs the cloud doesn’t have the requirements of the legacy enterprise allowing Ampere to design with a different point of view. Our hardware is being designed to take on these new technologies and cloud workloads, giving the industry a choice.

Ampere is working with communities like Packet, WorksOnArm and OpenJDK to build the software ecosystem, participating in open projects such as the Linux Kernel, gcc/llvm, OpenBMC and other cloud technologies in order to ensure broad compatibility with our eMAG platform.

Additionally, the Ampere Developer Program provides a community and resources including forums, documentation, video tutorials as well as the option to purchase the Ampere eMAG Development Platform and everything you need to get started.

Visit the developer site for more details. www.developer.amperecomputing.com

Experienced team

Renée James, Founder, Chairman and CEO
Renée James is a seasoned technology leader with large-scale, broad international operations experience. She is currently the chairman and CEO of Ampere Computing, a company she founded. Renée had a lengthy career with Intel Corporation where she was the president of the company until her departure in 2016. She is a current operating executive at the Carlyle Group.

Chi Miller, CFO and COO
Chi is the chief financial officer and chief operating officer of Ampere. Prior to joining Ampere, he was senior director of finance at Apple Corporation, where he supported the R&D group. Prior to Apple, Chi was with Intel for 25 years, where he held a variety of roles including platform engineering VP of finance, software and services VP of finance, M&A controller and server products controller.

Atiq Bajwa, Chief Architect
Atiq is a seasoned technologist with over 30 years of technical leadership experience in product R&D. Prior to joining Ampere, Atiq was VP and GM of product architecture at Intel, where he led the architectural definition and development of Intel’s computing products for the data center, PC, workstation and ultramobile markets. Before joining Intel, Atiq was a member of the team that developed the 32000 family of microprocessors at National Semiconductor.

Rohit Avinash Vidwans, Executive Vice President of Engineering
Rohit brings over 25 years of experience from Intel where he developed microprocessors, graphics, media and supercomputing processors and platforms, and holds 8 microprocessor design patents. Notable projects that he
worked on or led include Intel’s™ first 8 and 10 core Xeon™ microprocessors for data center and enterprise servers and Intel’s first multi-core phone and tablet SOCs based on the ATOM™ core.

**Stephan Jourdan, Ampere Fellow**
Stephan brings a wealth of experience in CPU, SoC and system architecture to Ampere. Prior to Ampere, he spent 21 years in product architecture at Intel, where he held a variety of technology and management roles including CPU core architect, chief architect of Intel’s PC client and mobile device SOCs, and most recently he was an Intel fellow in the Infrastructure and Platform Solutions Group (IPSG) and served as the IPSG chief technology officer. Stephan is a prolific inventor, with over 50 patents to his name. Stephan received his PhD from University of Toulouse, France, and an MBA from University of Oregon.

**Matt Taylor, SVP Worldwide Sales and Business Development**
Matt is the senior vice president of Worldwide Sales and Business Development at Ampere. Matt has 20 years of sales and business development experience in the data center and semiconductor industry. Prior to joining Ampere, Matt was the VP of Sales and Business Development for the Qualcomm Datacenter group, where he was responsible for Qualcomm’s first Cloud, OEM, and ODM design wins and revenue shipments of the Centriq platform. Prior to Qualcomm, Matt was with Intel for 15 years, where he held a variety of Sales, Marketing and Business Development leadership roles. Most notably Matt led Intel’s sales campaign for Cisco’s datacenter portfolio and then went on to develop and lead the Worldwide Amazon account team, growing Amazon into a multi billion dollar account for Intel.

**Mauri Whalen, Vice President Software Engineering**
Mauri Whalen is vice president of software engineering at Ampere. Her team leads and delivers software for tools/compilers, BIOS/UEFI, OS, hypervisors, middleware, application development and optimization for the Ampere™ eMAG platform. She also drives the developer program which allows developers to create and innovate on Ampere’s hardware platform. Prior to joining Ampere, she spent more than a decade in Intel’s Open Source Technology Center. Her most recent position was vice president of a core system development team, where she led efforts in open source software development across a range of technologies and market segments, including enterprise Linux, leadership in the Linux ecosystem, client Linux programs and related technologies.

**Jeff Wittich, Senior Vice President of Products**
Jeff Wittich is the senior vice president of Products at Ampere. Jeff has extensive leadership experience in the semiconductor industry in roles ranging from product and process development to business strategy to marketing. Prior to joining Ampere, he worked at Intel for 15 years in a variety of positions throughout the company. Most recently, he was responsible for the Cloud Service Provider Platform business, driving global market reach, product customization, and ultimately defining the products and platforms being used across the cloud worldwide. While at Intel, Jeff also led a product development team responsible for 5 generations of Xeon processors. He received an Intel Achievement Award for his work in developing the Custom CPU program. Jeff has an MS in Electrical and Computer Engineering from the University of California, Santa Barbara, and a BS in Electrical Engineering from the University of Notre Dame.