### Predictable Performance

Ampere Altra Max offers up to 128 cores operating at a maximum of 3.0 GHz. Each core is single threaded by design with its own 64 KB L1 I-cache, 64 KB L1 D-cache, and a huge 1 MB L2 cache, delivering predictable performance 100% of the time by eliminating the noisy neighbor challenge within each core.

Coherent mesh-based interconnect topology provides efficient bandwidth with 32 distributed home nodes and directory-based snoop filters to enable seamless connectivity between the cores.

Supporting eight, 2DPC, 72-bit DDR4-3200 channels, the Ampere Altra Max processor offers high bandwidth and memory capacity of up to 4 TB per socket.

### Power Efficiency

Ampere Altra Max provides industry-leading power efficiency/core, while packing 128 cores in a single-socket and 256 cores in a dual-socket platform, establishing new levels of power efficiency with scalability.

Ampere’s power optimized design, coupled with 7 nm process technology, enables the Ampere Altra Max processor to pack in more cores than any other datacenter class processor – all on a single die – enabling datacenter infrastructure providers more cores per rack.

Ampere Altra Max processor’s advanced power management capabilities include Advanced Configuration Power Interface (ACPI) v6.2 support, Dynamic Frequency Scaling (DFS), on-die thermal monitoring, and dynamic power estimation.

### High Scalability

With leading power/core, and multi-socket support, Ampere Altra Max provides the scalability to maximize the number of servers per rack, unparalleled in the industry.

With 128 lanes of PCIe Gen4 per socket with support for 192 PCIe Gen4 lanes in 2P configuration that can be bifurcated down to x4, Ampere Altra Max provides maximum flexibility to interface with off-chip devices, including networking cards up to 200 GbE or more, and storage/NVMe devices, making it well suited for big data applications.

Ampere Altra Max supports cache coherent connectivity to off-chip accelerators. 64 of the 128 PCIe Gen 4 lanes support Cache Coherent Interconnect for Accelerators (CCIX), that could be used for networking, storage, or accelerator connectivity.

### Reliability, Availability, and Serviceability (RAS)

The Ampere Altra Max processor provides extensive enterprise server-class RAS capabilities. Data in memory is protected with advanced ECC in addition to standard DDR4 RAS features. End-to-end data poisoning ensures corrupted data is tagged and any attempt to use it is flagged as an error. The SLC is also ECC protected, and the processor supports background scrubbing of the SLC cache and DRAM to locate and correct single-bit errors.

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<table>
<thead>
<tr>
<th><strong>Features</strong></th>
<th><strong>PROCESSOR SUBSYSTEM</strong></th>
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<tbody>
<tr>
<td></td>
<td>128 Armv8.2+ 64-bit CPU cores up to 3.0 GHz maximum</td>
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<td></td>
<td>64 KB L1 I-cache, 64 KB L1 D-cache per core</td>
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<td></td>
<td>1 MB L2 cache per core</td>
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<td></td>
<td>16 MB System Level Cache (SLC)</td>
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<td></td>
<td>2x full-width (128b) SIMD</td>
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<td></td>
<td>Coherent mesh-based interconnect – Distributed snoop filtering</td>
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<tr>
<th><strong>MEMORY</strong></th>
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<tr>
<td>8x 72-bit DDR4-3200 channels</td>
</tr>
<tr>
<td>ECC, Symbol-based ECC, and DDR4 RAS features</td>
</tr>
<tr>
<td>Up to 16 DIMMs and 4 TB/socket</td>
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<tr>
<th><strong>SYSTEM RESOURCES</strong></th>
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<tr>
<td>Full interrupt virtualization (GICv3)</td>
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<td>Full I/O virtualization (SMMUv3)</td>
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<tr>
<td>Enterprise server-class RAS</td>
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<tr>
<th><strong>CONNECTIVITY</strong></th>
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<tr>
<td>128 lanes of PCIe Gen4</td>
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<tr>
<td>– 4x16 PCIe + 4x16 PCIe/CCIX with Extended Speed Mode (ESM) support for data transfers at 20/25 GT/s</td>
</tr>
<tr>
<td>– 32 controllers to support up to 32 x4 links</td>
</tr>
<tr>
<td>128 PCIe lanes in 1P configuration</td>
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<tr>
<td>192 PCIe lanes in 2P configuration</td>
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<tr>
<td>Coherent multi-socket support</td>
</tr>
<tr>
<td>4x16 CCIX lanes</td>
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Ampere Altra Max™ Product Brief

**Ampere Altra Max Ecosystem**
Ampere’s Altra Max processors are supported by an extensive partner ecosystem of products and services from a wide range of leading suppliers, including industry standard providers of:
- Operating systems
- Hardware, and software development tools
- Evaluation platforms
- Technical training

Ampere offers Altra Max based Mt. Collins platforms for product evaluation and for early software development.

**Mt. Collins 1U and 2U Dual Socket Rack Server**
The soon to be available Mt. Collins platform provides a balance of performance and power that can scale up with ease. Mt. Collins provides 160 cores with predictable performance that is ideal for independent VMs and containers. The 1U Mt. Collins rack server is an excellent fit for rack environments that require peak memory and lower TCO.

The 2U Mt. Collins rack server is an excellent fit for Android in the Cloud, AI/ML and HPC usages. It is designed for rack environments that require peak memory, flexible storage, outstanding value, and intuitive management.

Visit www.amperecomputing.com to learn more about Ampere’s Altra Max processor.

**Ordering Information**
The ordering information for the currently available SKUs is listed below.
- AC-212825002 (128 cores, 250 W)
- AC-212823002 (128 cores, 230 W)
- AC-212819002 (128 cores, 190 W)
- AC-211224002 (112 cores, 240 W)
- AC-211221002 (112 cores, 210 W)
- AC-211218002 (112 cores, 180 W)
- AC-209623502 (96 cores, 235 W)
- AC-209622002 (96 cores, 220 W)
- AC-209619002 (96 cores, 190 W)
- AC-209617002 (96 cores, 170 W)

**SPECIFICATIONS**
- Operating Junction Temperature Range
  - 0°C to +90°C
- Power Supplies
  - CPU: 0.75 V, DDR4: 1.2 V
  - I/O: 3.3 V/1.8 V, SerDes PLL: 1.8 V
- Packaging
  - 4926-Pin FCLGA

**TECHNOLOGY & FUNCTIONALITY**
- Armv8.2+, SBSA Level 4
- Advanced Power Management
  - Dynamic estimation, Turbo Gen2, Voltage droop mitigation

**PERFORMANCE & POWER**
- Est. SPECrate® 2017_int_base: 350
- TDP: 250 W

**PROCESS TECHNOLOGY**
- TSMC 7 nm FinFET

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**Altra Max Block Diagram**

128 Arm v8.2+ @ 3.0 GHz

GICv3 Interrupt Controller

8x DDR4 – 3200

Coherent Mesh-based Interconnect + Distributed Snoop Directories

Arm SMMUv3 + GICv3 ITS

x16 PCIe Gen4

x16 PCIe Gen4

x16 PCIe Gen4 / CCIX

x16 PCIe Gen4 / CCIX

x16 PCIe Gen4 / CCIX

x16 PCIe Gen4 / CCIX

x16 PCIe Gen4 / CCIX

x16 PCIe Gen4 / CCIX

Low Speed I/O Interfaces

I2C

SPI

UART

Timers

GPIOs

GPIOs

Secure Boot and Management Processors

SMpro

PMpro

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